

# CIS 771: Software Specifications

## Lecture: Alloy Whirlwind Tour (part C)

*Copyright 2007, John Hatcliff, and Robby. The syllabus and all lectures for this course are copyrighted materials and may not be used in other course settings outside of Kansas State University in their current form or modified form without the express written permission of one of the copyright holders. During this course, students are prohibited from selling notes to or being paid for taking notes by any person or commercial firm without the express written permission of one of the copyright holders.*

CIS 771 --- Alloy Whirlwind Tour (part C)

## Outline

- Operations -- modeling state transitions
  - pre-states
  - post-states
- Operation contracts
- Address Book operations
  - add name & address entry
  - remove entry

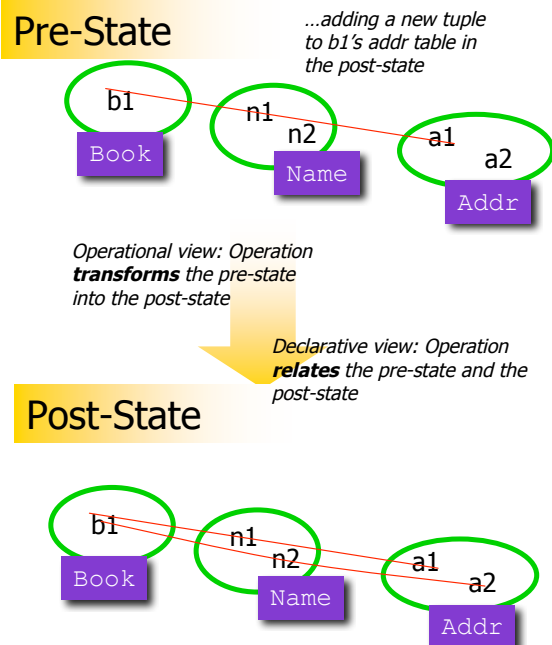
# Dynamic Models

- Static models allow us to describe the legal **states** of the system
- We also want to be able to describe the legal **transitions** between states
  - A (name,addr) pair are added to the address book
  - An entry is removed from the address book
  - A new book is created

CIS 771 --- Alloy Whirlwind Tour (part C)

## Modeling State Transitions

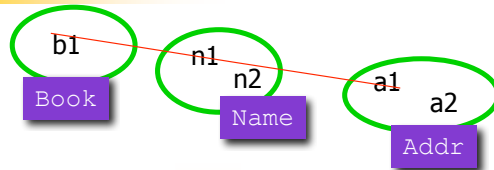
- In declarative modeling,
  - there is no "assignment operator"
  - we don't do "destructive updates" or "state mutations"
- For every operation execution or state transition, there is a...
  - pre-state -- the state before the operation is executed
  - post-state -- the state after an operation is executed



CIS 771 --- Alloy Whirlwind Tour (part C)

# Example Operations

## Pre-State

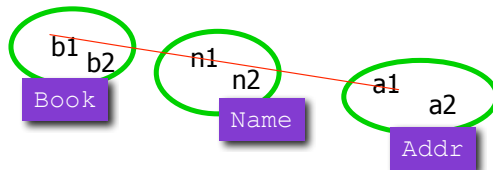


...adding a new book

Operational view: Operation **transforms** the pre-state into the post-state

Declarative view: Operation **relates** the pre-state and the post-state

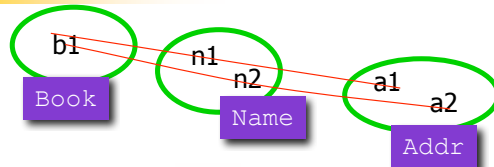
## Post-State



CIS 771 --- Alloy Whirlwind Tour (part C)

# Example Operations

## Pre-State

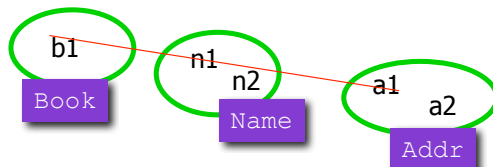


...removing an address book entry

Operational view: Operation **transforms** the pre-state into the post-state

Declarative view: Operation **relates** the pre-state and the post-state

## Post-State



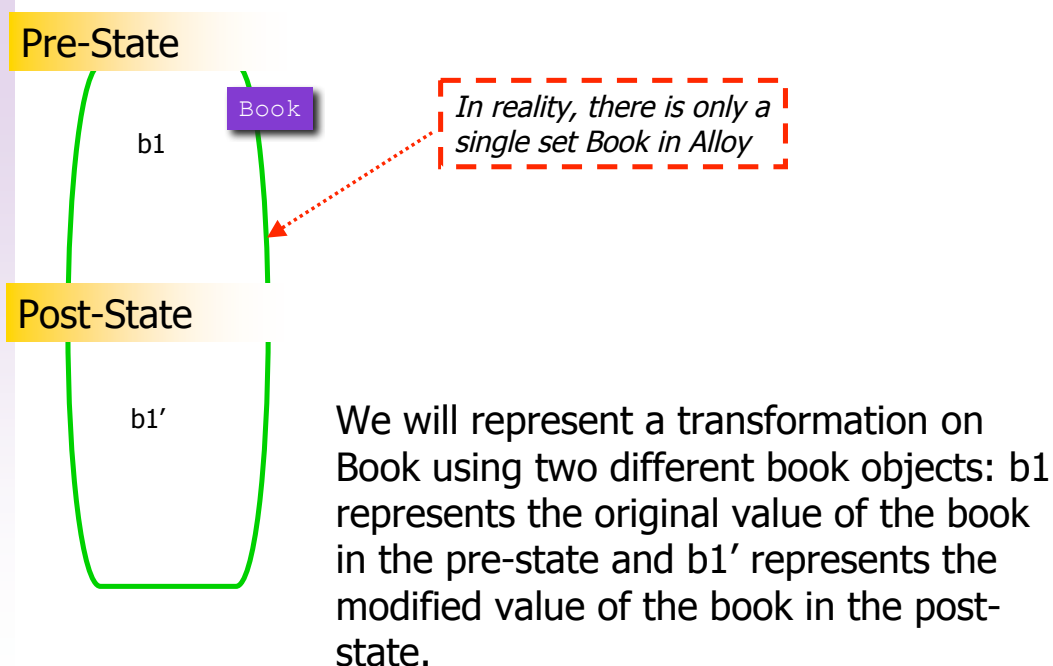
CIS 771 --- Alloy Whirlwind Tour (part C)

# Modeling State Transitions

- Alloy only has one "state"
  - e.g., there is really only one set of objects for each signature
- We will simulate having both a pre-state and post-state (and later on, a sequence of states) by using some conventions for variable names
  - `b.addr` -- represents b's address field in the pre-state
  - `b'.addr` -- represents b's address field in the post-state

CIS 771 --- Alloy Whirlwind Tour (part C)

# Modeling State Transitions



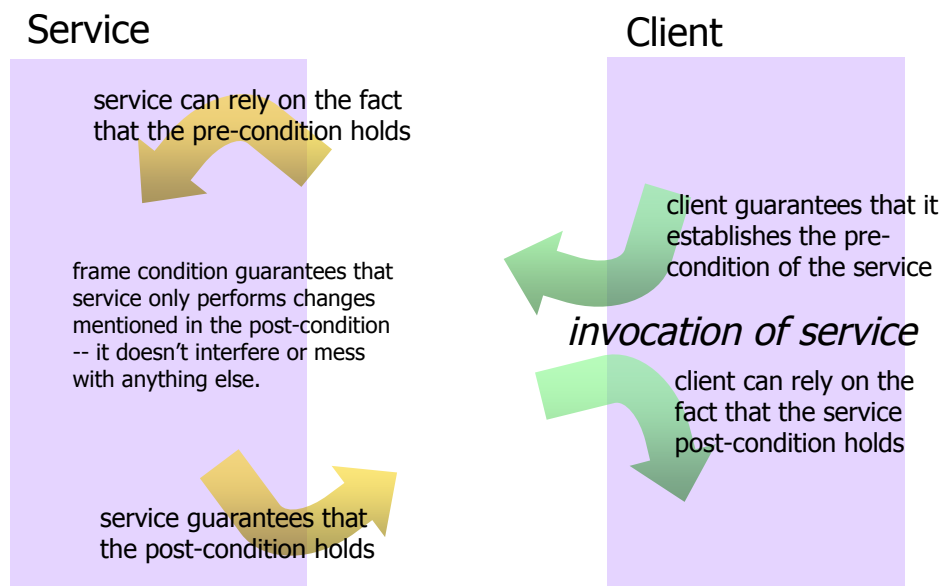
CIS 771 --- Alloy Whirlwind Tour (part C)

# Modeling State Transitions

- Specifications should generally focus on “what” instead of “how”
- Our specification of operations will focus on
  - Pre-conditions: what properties are expected/assumed of the pre-state if the operation is to work correctly
    - e.g., an address book delete of person N might assume that N already exists in the book
  - Post-conditions: what properties does the operation establish in the post-state
    - e.g., N and any associated addresses don't exist in the address book after delete
  - Frame-conditions: what stayed the same
    - e.g., no other entries in the current book were removed

CIS 771 --- Alloy Whirlwind Tour (part C)

# Software Contracts



CIS 771 --- Alloy Whirlwind Tour (part C)

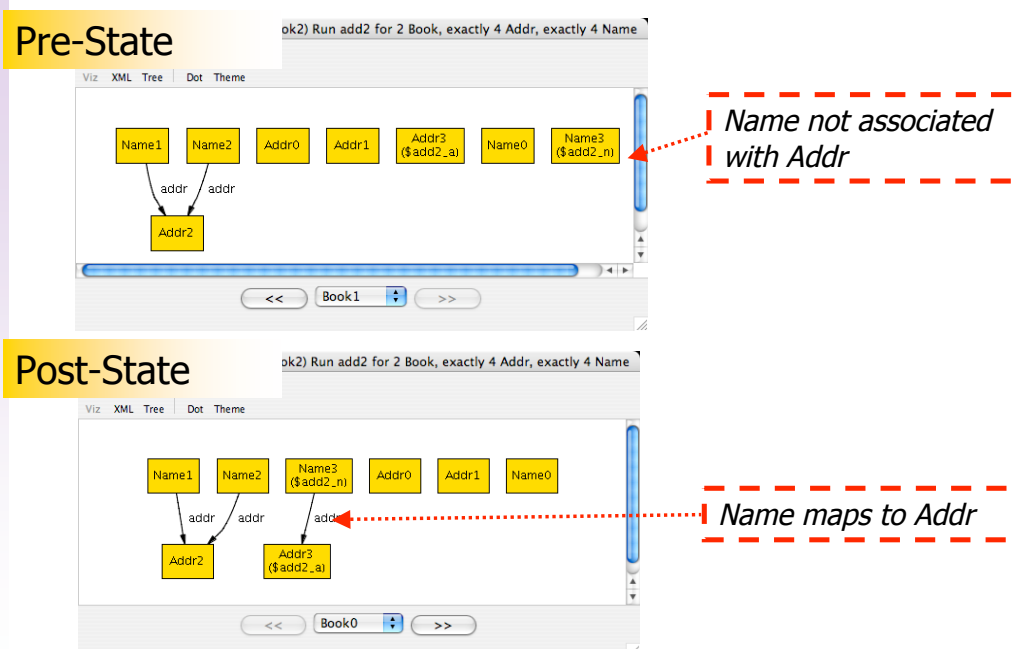
# Modeling *Add Address*

```
pred add2(b, b': Book, n:Name, a:Addr) {  
  // pre-condition  
  // n is not currently in the book  
  no n.(b.addr)  
  
  // post-condition  
  // invoking b' addr map on n yield's a  
  n.(b'.addr) = a  
  
  // frame-condition  
  // for all other names, the addr map should  
  // yield the same value  
  all n1: (Name - n) | n1.(b.addr) = n1.(b'.addr)  
}
```

CIS 771 --- Alloy Whirlwind Tour (part C)

## Results

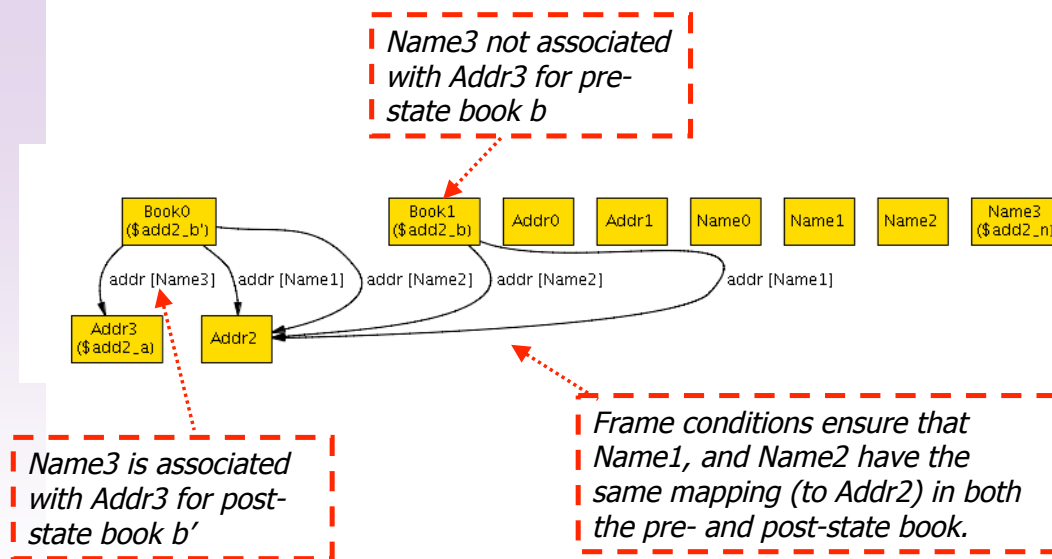
*using project over Book sig  
option in visualization*



**run add2 for 2 Book, exactly 4 Name, exactly 4 Addr**  
CIS 771 --- Alloy Whirlwind Tour (part C)

# Results (alternate)

*without project over Book sig option in visualization*



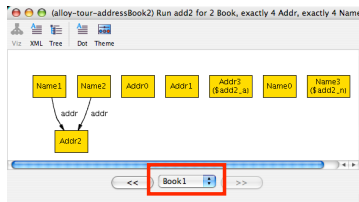
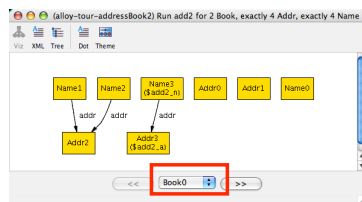
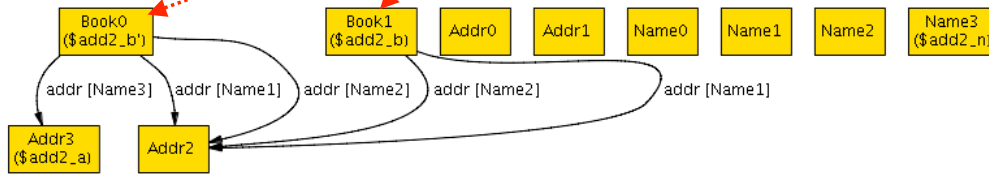
run add2 for 2 Book, exactly 4 Name, exactly 4 Addr  
CIS 771 --- Alloy Whirlwind Tour (part C)

## Assessment

- The tool support and methodology for modeling operations is not perfect
  - default ordering that Alloy assigns to generated names does not necessarily follow pre/post order
  - Naming convention using primes is just a convention -- Alloy really doesn't understand that, e.g.,  $b$  and  $b'$  should be *different* versions of the same object. Sometimes you may need to force  $b \neq b'$ .

# Assessment

*Pre-state book is named Book1 and post-state book is named Book0*



...therefore, in project over Book visualization, the post-state gets displayed before the pre-state

CIS 771 --- Alloy Whirlwind Tour (part C)

# Assessment

Consider the following definition of add...

```
pred add(b,b': Book, n: Name, a: Addr) {
    b'.addr = b.addr + n->a
}
```

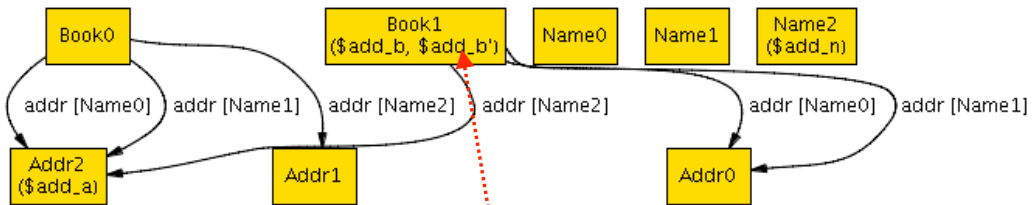
```
run add for 3 but 2 Book
```

*addr map of b in the post-state (b') equals addr map of b in the pre-state unioned with the (n,a) tuple*

CIS 771 --- Alloy Whirlwind Tour (part C)



# Assessment



*b and b' are just names. Alloy doesn't understand that they should represent two different objects, and so they both can bound to the same object (there is no difference between the pre- and post-state. This is technically correct because this version of the add operation does not require that the name be absent from the addr book before adding.*

CIS 771 --- Alloy Whirlwind Tour (part C)

# Assessment

Consider the following definition of add...

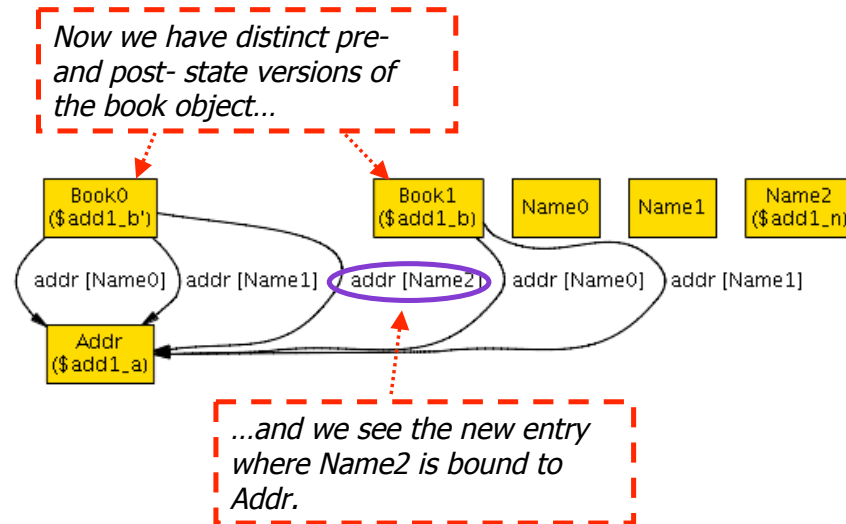
```
pred add(b,b': Book, n: Name, a: Addr) {
  b != b'
  b'.addr = b.addr + n->a
}
```

```
run add for 3 but 2 Book
```

*add a constraint to force representation of the pre- and post- state books using two different objects*

CIS 771 --- Alloy Whirlwind Tour (part C)

# Assessment



CIS 771 --- Alloy Whirlwind Tour (part C)

# Delete Operation

Version of Delete without precise pre/post-conditions..

```
pred del(b,b': Book, n: Name) {  
  b'.addr = b.addr - n->Addr  
}
```

relation  
representing  
addr table

set difference  
operation

set of all tuples in which n is paired  
with an address from Addr

Intuition: take the entries in b.addr and throw away all tuples in which n is paired with any address.

CIS 771 --- Alloy Whirlwind Tour (part C)

## For You To Do

- Visualize the behavior of the delete operation on the previous slide
- Add any constraints and/or make scope adjustments as we have discussed previously to create an interesting instance

CIS 771 --- Alloy Whirlwind Tour (part C)

## Lookup Operation

Using the Alloy function construct

```
fun lookup(b: Book, n: Name) {  
  n.(b.addr) ←  
}
```

*value of this expression is the  
"return value" for the function*

- Since lookup returns a value, use an Alloy function construct instead of a predicate
- Note: lookup is "read only" -- it doesn't cause a state transformation. Therefore, we have no "primed" variables.

CIS 771 --- Alloy Whirlwind Tour (part C)

# Checking Operation Composition

## Using the Alloy assertion construct

```
assert delUndoesAdd {  
  all b,b',b'': Book, n:Name, a:Addr |  
    add[b,b',n,a] and del[b',b'',n]  
    implies b.addr = b''.addr  
}
```

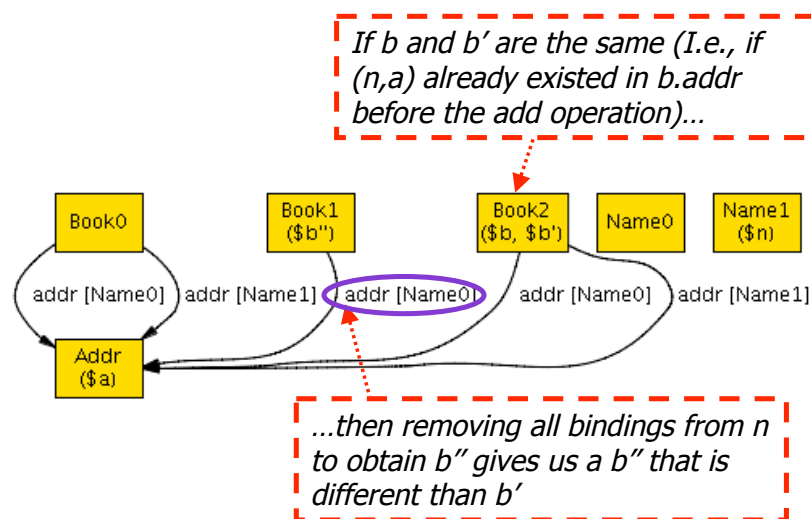
```
check delUndoesAdd for 3
```

- Intuition: we claim (assert) that if we add (n,a) to b to get b', then remove from b' any associations with n to get b'', then the addr mapping of b and b'' should be equal.
- run/pred tells the analyzer to look for an example that satisfies pred
- check/assert tells the analyzer to look for a *counterexample* that falsifies assert

CIS 771 --- Alloy Whirlwind Tour (part C)

## Result

### A counterexample (assertion violation) is found...



This outcome is tied to our previous observation: the definition of add did not include a precondition requiring  $n$  to be absent from the book.

CIS 771 --- Alloy Whirlwind Tour (part C)

# Checking Operation Composition

## Modified version...

```
assert delUndoesAdd {  
  all b,b',b'': Book, n:Name, a:Addr |  
    |no n.(b.addr)| and  
    add[b,b',n,a] and del[b',b'',n]  
    implies b.addr = b''.addr  
}
```

```
check delUndoesAdd for 3
```

- ...now the analyzer finds no counter-examples

CIS 771 --- Alloy Whirlwind Tour (part C)

# Checking Operation Composition

## Assertion checking idempotency of add

```
assert addIdempotent {  
  all b,b',b'': Book, n:Name, a:Addr |  
    add[b,b',n,a] and add[b',b'',n,a]  
    implies b'.addr = b''.addr  
}
```

- Intuition: repeating the add operation yields no additional effect
- Outcome: no counterexamples found

CIS 771 --- Alloy Whirlwind Tour (part C)

# Checking Operation Composition

## Assertion checking non-interference of add

```
assert addLocal {  
  all b,b': Book, n,n':Name, a:Addr |  
    add[b,b',n,a] and n != n'  
    implies lookup[b,n'] = lookup[b',n']  
}
```

- Intuition:
  - checks the frame condition for add that we discussed earlier
  - adding an address for n doesn't change the bindings for any of the other names in the book
- Outcome: no counterexamples found

CIS 771 --- Alloy Whirlwind Tour (part C)

# Conclusions

- In addition to specifying the legal **states** of a system, Alloy enables us to describe the legal **transitions** between states.
  - We described such transitions as operations that take as input a *pre-state* and return a *post-state*.
- In high-level specifications, we aim to specify *what* abstraction properties/constraints a state transition (operation) must satisfy -- not *how* the operation is actually carried out.
- Declarative specifications of operations are often broken down into three parts
  - pre-conditions (properties that we assume about the pre-state)
  - post-conditions (properties that the operation guarantees of the post-state)
  - frame-conditions (constraints that identify explicitly which portions of the state are allowed to be modified by the operation).

CIS 771 --- Alloy Whirlwind Tour (part C)

## For You To Do

- Define an alternative version of the `del` operation that includes pre-, post-, and frame-conditions similar to the definition `add2` given in this lecture.
- Analyze your definition using several scope settings to assess its validity.
- Create an assertion `delidempotent` (that applies to the version of delete that you defined above) similar to `addidempotent` assertion defined in this lecture and check the assertion using several different scope settings. What can you conclude about the validity of the assertion?

CIS 771 --- Alloy Whirlwind Tour (part C)

## Acknowledgements

- The material in this lecture is based on Section 2.2 from...
  - *Software Abstractions: Logic, Language, and Analysis*, Daniel Jackson, MIT Press, 2006.

CIS 771 --- Alloy Whirlwind Tour (part C)